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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Attorney Docket No. Castro 1In re application of: Peter R. CastroSerial No.: 09/311,369Group Art Unit: 2662Filed: 05/13/99Examiner: Thai D. HoangMatter No.: 990.0189Phone No.: 703-305-3232For: Fault Protection for Hitless and Errorless Switching of Telecommunications SignalsAMENDMENT UNDER 37 CFR 1.111Commissioner for Patents  
Washington, D.C. 20231This Amendment is filed in response to the office action of 04/25/02.IN THE SPECIFICATION

- A-1
- o Insert new paragraph after page 3, line 31, as follows:

Fig. 5 shows a flow diagram of the processing of each checkbit generator in Fig. 3, according to one embodiment of the present invention.

- A-2
- o Insert new paragraph after page 7, line 16, as follows:

Fig. 5 shows a flow diagram of the processing of each checkbit generator 306 in Fig. 3, according to one embodiment of the present invention. In particular, checkbit generator 306 divides the corresponding sub-signal into one or more subsets of data (step 502 of Fig. 5), applies a checksum function to each subset of data to generate a checkbit for the subset (step 504), adds the checkbit for each subset to the sub-signal to generate an augmented sub-signal (step 506), and transmits at least two copies of the augmented sub-signal (step 508) for routing in parallel through redundant portions of a distributed switch fabric to generate at least two routed sub-signals for the sub-signal.

Certification Under 37 CFR 1.8Date of Deposit 5/14/02

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail under 37 CFR 1.8 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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